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Application / Conf. No.	09/991,412 / 3726
Filing Date	November 16, 2001
First Named Inventor	Nigel G. Herron
Examiner Name	Unknown
Group Art Unit	2819
Issue Fee Batch No.	
Attorney Docket Number	X-916 US

ENCLOSURES (check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	H. C. Chan	24309 (Customer Number)	Reg. Number 35,477
Signature			
Date	October 1, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040	

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Typed or Printed Name	Pat Slaback		
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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Nigel G. Herron et al.
Assignee: Xilinx, Inc.
Title: "Method and Apparatus for Testing Circuitry Embedded within a Field Programmable Gate Array"
Serial No.: 09/991,412 File Date: 11/16/01
Examiner: Unknown Art Unit: 2819
Docket No.: X-916 US Conf. No.: 3726

COMMISSIONER FOR PATENTS
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form PTO-1449A/PTO. A copy of the reference is enclosed.

This Supplemental Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

H. C. Chan
Attorney for Applicants
Reg. No. 35,477

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Pat Slaback
Name

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